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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/037,454	12/31/2001	Joseph Cleveland	SAMS01-00198 4821	
7590 07/11/2005 DAVIS & MUNCK, P.C. P.O. Drawer 800889			EXAMINER TSE, YOUNG TOI	
			Dallas, TX 75	380
			2637	
			DATE MAILED: 07/11/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)				
Office Action Commence	10/037,454	CLEVELAND ET AL.				
Office Action Summary	Examiner	Art Unit				
	YOUNG T. TSE	2637				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	i6(a). In no event, however, may a reply be tim within the statutory minimum of thirty (30) days ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONEI	ely filed  will be considered timely.  the mailing date of this communication.  (35 U.S.C. § 133).				
Status		•				
1)⊠ Responsive to communication(s) filed on 31 De	ecember 2001.					
2a)☐ This action is <b>FINAL</b> . 2b)☒ This	This action is <b>FINAL</b> . 2b)⊠ This action is non-final.					
3) Since this application is in condition for allowar	• • • • • • • • • • • • • • • • • • • •					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4) ⊠ Claim(s) <u>1-24</u> is/are pending in the application. 4a) Of the above claim(s) is/are withdray 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) <u>1-24</u> is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and/or		;				
Application Papers						
9) The specification is objected to by the Examine 10) The drawing(s) filed on 31 December 2001 is/an Applicant may not request that any objection to the Replacement drawing sheet(s) including the correction 11) The oath or declaration is objected to by the Ex	re: a) $\square$ accepted or b) $\square$ object drawing(s) be held in abeyance. See on is required if the drawing(s) is obj	e37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list	s have been received. s have been received in Application ity documents have been receive I (PCT Rule 17.2(a)).	on No ed in this National Stage				
Attachment(s)  1) Notice of References Cited (PTO-892)  2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) Interview Summary Paper No(s)/Mail Da	ite				
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  Paper No(s)/Mail Date	5)  Notice of Informal P 6)  Other:	atent Application (PTO-152)				

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## **DETAILED ACTION**

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## Specification

- The disclosure is objected to because of the following informalities: on page 1, the Applicants are requested to update the copending application serial numbers.
   Appropriate correction is required.
- 2. The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

## Claim Objections

3. Claims 1-22 are objected to because of the following informalities: in claim 1, lines 13, 27 and 30, "[+1,+1]" and "[-1,-1]" are suggested to change to "(+1,+1)" and "(-1,-1)". Also suggest the same changes to claims 3, 5, 7, 13, 15, 17, and 19. Wherein the dependent claims 2, 4, 6, 8-12, 14, 16, and 20-24 depend upon the independent claims 1 and 13. Appropriate correction is required.

# Claim Rejections - 35 USC § 112

4. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

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5. Claims 1-22 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claims 1 and 13, "S", "M", "N", "S", "kth", and "ith" are undefined.

In claim 1, lines 11-12, 24, and 29, the Applicants are requested to clarify the difference of "a logic 00 value". Also see claims 2-7 and 14-19 for "a logic 01 value", "a logic 10 value", and "a logic 11 value".

Wherein dependent claims 8-12 and 20-24 depend upon the independent claims 1 and 13.

## Allowable Subject Matter

- 6. Claims 1-22 would be allowable if rewritten or amended to overcome the objections and the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action.
- 7. The following is a statement of reasons for the indication of allowable subject matter: Haines discloses a demodulator circuit comprising clock delay lines and a switch enable signal to enable one of a plurality of switches to generate a demodulated signal. However, the prior art fails to show or suggest a demodulator comprises the combination of a storage array capable of storing S logic 00 code masks, each of the S logic 00 code masks associated with one of the S orthogonal modulation codes, wherein a kth logic 00 code mask comprises M/2 logic 00 code mask bits, each of the M/2 logic 00 code mask bits is associated with a corresponding one of M/2 sequential

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pairs of M binary bits in a kth orthogonal modulation code, wherein the each M/2 logic 00 mask bit is a logic 1 if the corresponding sequential pair of the M binary bits in the kth orthogonal modulation code is equal to the logic 00 value and is equal to logic 0 otherwise and a decision circuit capable of detecting a (+1,+1) signal output by the logic 00 input detector after a comparison of a jth sequential pair of the M/2 sequential pairs of the M binary bits to the logic 00 value and, in response to the detection, adding the (+1,+1) signal to a jth one of the M/2 logic 00 code mask bits in the logic 00 code mask in the storage array is equal to 1.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to YOUNG T. TSE whose telephone number is (571) 272-3051. The examiner can normally be reached on Monday and Wednesday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on (571) 272-2988. The Central FAX Number for the organization where this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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OUNG T. TSE
Primary Examiner